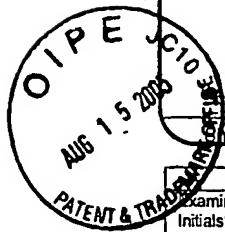


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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	10/731,832
		Filing Date	12/09/2003
		First Named Inventor	Seyed-Ali Hajimiri
		Art Unit	2817
		Examiner Name	Khanh V. Nguyen
		Attorney Docket Number	13641.0060
Sheet	1	of	4



## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

Sheet 1 of 4

Application Number 10/731,83

Filing Date 13/08/2003

First Named Inventor: Sayed Ali Halimiri

First Wanted Inventor: Seyer

Atom 281  
Business Name Kbaab

Examiner Name: Khanh V. Nguyen  
Attorney Serial Number: 123456789

Attorney Docket Number 13641

## DOCUMENTS

**DOCUMENTS** **Name of Patentee or** **Pages, Co**

## U. S. PATENT DOCUMENTS

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
N/kr		Number-Kind Code <sup>2</sup> (if known)			
N/kr	US-	US- 4,001,711	01-04-1977	Knutson et al.	
	US-	US- 6,366,166 B1	04-02-2002	Belot	
	US-				

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code <sup>2</sup> -Number <sup>3</sup> -Kind Code <sup>4</sup> (if known)	MM-DD-YYYY		T <sup>5</sup>
N/m	JP 07204367		10-08-1995	Mitsui Yasuro	
N/m	JP 54172531		12-28-1979	Kanemoto Yoshitaka	

Examiner  
Signature

NGUYEN, KV

Date  
Considered

10/12/03

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				Art Unit	2817
				Examiner Name	Khanh V. Nguyen
				Attorney Docket Number	13641.0060

<b>NON PATENT LITERATURE DOCUMENTS</b>			
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Nh	1	International Search Report for PCT/US03/39174, May 5, 2004.	
	2	D.K. SHAEFFER, ET AL., "A 1.5-V, 1.5-GHz CMOS Low Noise Amplifier," IEEE Journal of Solid-State Circuits, IEEE Inc., May 1997, pp. 745-749, Vol. 32, No. 5, New York, NY.	
	3	H. DARABI, ET AL., "A 4.5-mW 900-MHz CMOS Receiver for Wireless Paging," IEEE Journal of Solid-State Circuits, August 2000, pp. 1085-1096, Vol. 35, No. 8.	
	4	S. WU, ET AL., "A 900-MHz/1.8-GHz CMOS Receiver for Dual-Band Applications," IEEE Journal of Solid State Circuits, December 1998, pp. 2178-2185, Vol. 33, No. 12.	
	5	A. ROFOUGARAN, ET AL., "A 1 GHz CMOS RF Front-End IC for a Direct-Conversion Wireless Receiver," IEEE Journal of Solid-State Circuits, July 1996, pp. 880-889, Vol. 31, No. 7.	
	6	J.C. RUDELL, ET AL., "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, December 1997,	
		pp. 2071-2088, Vol. 32, No. 12.	
	7	H. SAMAVATI, ET AL., "A 5-GHz CMOS Wireless LAN Receiver Front End," IEEE Journal of Solid-State Circuits, May 2000, pp. 765-772, Vol. 35, No. 5.	
✓	8	B.A. FLOYD, ET AL., "A 23.8-GHz SOI CMOS Tuned Amplifier," IEEE Transactions on Microwave Theory and Techniques, September 2002,	
Nh		pp. 2193-2196, Vol. 50, No. 9.	

Examiner Signature	NGUYEN, KV	Date Considered	10/12/05
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Sheet

3

of

4

Application Number	10/731,832
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First Named Inventor	Seyed-Ali Hajimiri
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Examiner Name	Khanh V. Nguyen
Attorney Docket Number	13641.0060

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
Nh	9	P: LEROUX, ET AL., "A 0.8-dB NF ESD-Protected 9-mW CMOS LNA Operating at 1.23 GHz," IEEE Journal of Solid-State Circuits, June 2002, pp. 760-765, Vol. 37, No. 6.	
	10	S.F. TIN, ET AL., "A Simple Subcircuit Extension of the BSIM3v3 Model for CMOS RF Design," IEEE Journal of Solid-State Circuits, April 2000, pp. 612-624, Vol. 35, No. 4.	
	11	H. HASHEMI, ET AL., "Concurrent Multiband Low-Noise Amplifiers - Theory, Design, and Applications," IEEE Transactions on Microwave Theory and Techniques, January 2002,	
		pp. 288-301, Vol. 50, No. 1.	
	12	B.A. FLOYD, ET AL., "A 15-GHz Wireless Interconnect Implemented in a 0.18 (mu)M CMOS Technology Using Integrated Transmitters, Receivers, and Antennas," 2001 Symposium on VLSI	
		Circuits Digest of Technical Papers, June 2001, pp. 155-158.	
	13	X. GUAN, ET AL., "A 24GHz CMOS Front-end," in Proc. 28th ESSCIRC, September 2002, pp. 155-158.	
	14	S.M. ALAMOUTI, "A Simple Transmit Diversity Technique for Wireless Communications," IEEE Journal on Select Areas in Communications, October 1998, pp. 1451	
✓		-1458, Vol. 16, No. 8.	
Nh	15	D. LU, ET AL., "A 24-GHz Active Patch Array," International Journal of Infrared and Millimeter Waves, May 2002, pp. 693-704, vol. 23.	

Examiner Signature	NGUYEN, KV	Date Considered	10/12/05
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				Art Unit	2817
				Examiner Name	Khanh V. Nguyen
Sheet	4	of	4	Attorney Docket Number	13641.0060

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Ma	16	D.K. SHAEFFER, ET AL., "A 1.5V, 1.5GHz CMOS Low Noise Amplifier," 1996 Symposium on VLSI Circuits Digest of Technical Papers, June 1996, pp. 32-33.	
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	18	J.C. LIBERTI, ET AL., "Smart Antennas for Wireless Communications: IS-95 and Third Generation CDMA Application," 1999, pp. 83-88, Prentice Hall, New Jersey.	
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		pp. 1191-1193, vol. 32, no. 13.	
	20	D.K. SCHAEFFER, ET AL., "The Design and Implementation of Low-Power CMOS Radio Receivers," 1999, pp. 52-67, Kluwer Academic Publishers, Boston.	
↓	21	Y.C. HO, ET AL., "Implementation and Improvement for RF Low Noise Amplifiers in Conventional CMOS Technologies," 2000, Ph.D Thesis, University of Florida, Florida	
WV	22	A. VAN DER ZIEL, "Noise in Solid State Devices and Circuits," 1986, pp. 88-91, New York: Wiley.	

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